COMB: A Portable Benchmark Suite for Assessing MPI Overlap

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Abstract

This paper describes a portable benchmark suite that assesses the ability of cluster networking hardware and software to overlap MPI communication and computation. The Communication Offload MPI-based Benchmark, or COMB, uses two methods to characterize the ability of messages to make progress concurrently with computational processing on the host processor(s). COMB measures the relationship between MPI communication bandwidth and host CPU availability.

1. Introduction

Advances in networking technology for cluster computing have led to significant improvements in achievable latency and bandwidth performance. Many of these improvements are based on an implementation strategy called Operating System Bypass, or OS-bypass, which attempts to increase network performance and reduce host CPU overhead by offloading communication operations to intelligent network interfaces. These interfaces, such as Myrinet [3], are capable of “user-level” networking, that is, moving data directly from an application’s address space without any involvement of the operating system in the data transfer.

Overhead has been shown to be the most significant factor in affecting application performance [4]. Unfortunately, the reduction in host CPU overhead has not always been ac-

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accurate and detailed metrics for realizing specific performance enhancements which contribute to the ultimate goal of comprehensive performance.

COMB avoids the non-portable and/or less than accurate methods of measuring overhead such as a counter in the idle loop of the kernel or in a separate user-level thread, or such as the use of system contextual information. Kernel counters are very accurate but not portable. Separate user-level threads include overhead due to context switching. System-based contextual information, like pstat, is system dependent and can have a granularity dependent on the the OS scheduler.

COMB avoids these issues by using one process on each of two nodes. One process, the support process, simply supports communication while the other process, the timing process, communicates as well as monitors its rate of progress in for-loop iterations. This two-node communication is employed in two distinct methods: Polling and Post-Work-Wait as illustrated in Figure 1. Both methods actually run in two phases. During the first phase, the dry work phase, the method records the amount of time to accomplish a predetermined amount of work in the absence of communication. The second wet work phase records the time for the same amount of work while the two processes are exchanging messages. The CPU availability is reported as:

\[
\text{availability} = \frac{\text{time( work without messaging )}}{\text{time( work plus MPI calls while messaging )}}
\]

The Polling method periodically polls with MPI_Test for message arrival. After some number of iterations, the timing process polls for message arrival; this number of iterations defines the polling interval. If a test for message completion is negative, the timing process will iterate through another polling interval before testing again. If a test for completion is positive, the process will post related messaging calls and will similarly address any other received messages before entering another polling interval. The support process sends messages as fast as they are consumed. The Polling method terminates when a pre-determined number of accumulated iterations completes.

The essentially artificial communication model of the Polling method is aimed at gaging the maximum possible availability of the CPU as experienced by a user-level application under a continuous communication environment. In contrast, the Post-Work-Wait method models the exchange of two messages between nodes where only the essential non-blocking calls are made before some computational work (i.e., for-loop iterations) which is finally followed by a wait for message completion. The number of “work” iterations may be varied for a view into how the communication-specific time is spent during the non-blocking post, work, or wait periods. Again, the method is illustrated in Figure 1.

3. Platform description

The results presented here are based on the same node and network hardware using a variety of communication software. Each node contained a 500 MHz Intel Pentium III processor with 256MB of main memory and a Myrinet [3] LANai 7.2 network interface card (NIC). Nodes were connected using a Myrinet 8-port SAN/LAN switch. As discussed in detail below, we use two types of communication systems: MPICH/GM and MPI/Portals. These systems represent different communication paradigms and herein serve to illustrate the system characterization afforded by COMB rather than serving as points supporting a position on which paradigm is better.

MPICH/GM GM [2] is the supported message passing software from Myricom for Myrinet. It consists of a user-level library, a Linux driver and Myrinet Control Program (MCP) which runs on the NIC. Myricom also supplies a port of the MPICH [8] implementation of the MPI Standard. Our results were gathered using GM version 1.4, MPICH/GM version 1.2.4, and a Linux 2.2.14 kernel. This setup makes use of the intelligent NIC such that payload transfer to and from user-space occurs with little host involvement.

MPI/Portals Results were also gathered using the Portals 3.0 [7, 6] software designed and developed by Sandia and the University of New Mexico. Portals is an interface for data movement designed to support massively parallel commodity clusters, such as the Computational Plant [5]. We have also ported the MPICH implementation of MPI to Portals 3.0.
The particular implementation of Portals for Myrinet used in our experiments is kernel-based. The user-level Portals library interfaces to a Linux kernel module that processes Portals messages. This kernel module in turn interfaces to another kernel module that provides reliability and flow control for Myrinet packets. This kernel module works with a Sandia-developed MCP that simply acts as a packet engine. This particular implementation of Portals does not employ OS-bypass techniques.

4. Results and analysis

![Figure 2. Polling: bandwidth per message size (MPICH/GM)](image)

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![Figure 3. Polling: availability per message size (MPICH/GM)](image)

**Figure 3. Polling: availability per message size (MPICH/GM)**

Figures 2 and 3 show bandwidth and availability results, or equivalently overhead results, for using MPICH/GM using a short poll interval which ensures a maximum possible messaging rate. The pair of data points for a particular message size in these two graphs result from the same timing run; the experienced availability, or equivalently overhead, is concurrently measured with network bandwidth. For these two graphs, the polling interval is sufficiently small in each run such that the bandwidth graph represents the maximum sustainable bandwidth for that message size. Note that the graphs bring out system characteristics with respect to MPICH/GM’s protocol change at 16 KB, to another system pattern that repeats every 16 KB between message sizes between 16 KB and 128 KB, as well as to a second MPICH/GM protocol change at 128 KB. The run-time for gathering this data is approximately five minutes on our hardware platform.

4.1. Communication and computation overlap

When taken together, Figures 2 and 3 clearly illustrate the potential in MPICH/GM to overlap communication and computation. This represents MPICH/GM’s actual overlap under Polling’s artificially frequent calls to MPI/Test and the representation is made in terms of actual CPU availability while communication is in progress. Alternatively and as previously discussed, the Post-Work-Wait method model is closer to a realistic communication pattern between two nodes; during the exchange of two messages, the timing process performs work after the set of non-blocking calls and before the wait for message completion. A similar experimental technique has been used elsewhere [1].

Since Post-Work-Wait times the durations of the three periods, consider the duration of the wait period as a function of increasing work interval. If a communication system can overlap communication and computation with only the initiating posts then the wait duration should decrease with a longer and longer work interval. Figure 4 shows this function for the MPICH/GM and MPI/Portals systems. Clearly, without additional calls to the MPI library, MPICH/GM does not have overlap whereas MPI/Portals does.

Again, note that this is not an overall system characterization but a specific qualification of the ability of a system to overlap communication. In other comparisons, MPICH/GM has comparatively good performance in terms of raw bandwidth and general lack of communication overhead and subsection 4.2 illustrates the communication overhead associated with MPI/Portals.

4.2. Communication overhead

Figure 5 shows the Post-Work-Wait timing signature for MPI/Portals with 100 KB messages. The “y” axis is used to indicate the durations for the three periods of the method and the “x” axis indicates the increasing interval between
the non-blocking posts and the wait for message completion. As previously discussed, the wait durations decline with increasing work interval showing that MPI/Portals is able to overlap communication and computation.

What is also shown is the effect of overhead on delaying computation. Such overhead is indicated by the difference between the work without message handling (“work”) and work with message handling (“work (MH)”). Also, the overhead associated with the non-blocking posts is evident as the difference between a) the work with message handling and b) the sum of the non-blocking posts and work with message handling (“post work”). This detailed representation of the effects of communication are an aid to the developer in gaging efforts at performance improvement for specific periods within a communication cycle.

5. Summary

In this paper, we have described the COMB benchmark suite that characterizes a systems communication overhead as well as the ability of a system to overlap computation and communication. We have described the methods and approach of COMB and demonstrated its utility in providing insight into the underlying implementation of communication system. In particular, we have demonstrated the benchmark suite’s ability to distinguish between systems that support the MPI Progress Rule and those that do not. COMB is available at http://www.cs.unm.edu/~maccabe.

References