

Motivation

Performance degradation of HPC applications is caused by several factors:

- Host processor overhead due to communication processing
- Memory latency on inbound network data
- Cost of splitting OS functionality between host and NIC
- Data placement overhead (memory copies)
- Overhead due to external interrupts

Poor interaction of the NIC with the OS and applications, leading to poor performance

Goal

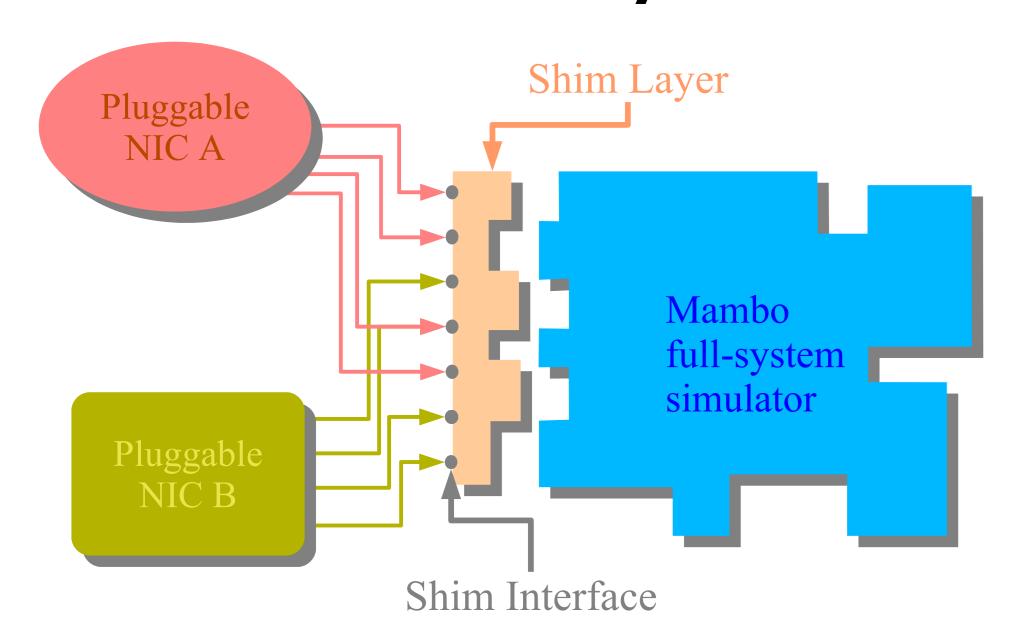
Build an infrastructure to:

- Study NIC/OS/Application interaction
 - Cache Injection
 - OS and Hypervisor bypass
 - Protocol Offloading
 - Interrupt direction and filtering
- Develop and evaluate next-generation NICs

Network Infrastructure

Framework to create simulated NICs

- Run arbitrary functionality
- Created as dynamic libraries
- *Plug-in* to IBM's Mambo full-system simulator
- Interact with host through the *Shim Layer*:
- Provides the glue between NIC and host
- Simulated NIC is developed without the need of Mambo source code
- Entry points explicitly defined by the *Shim* Interface



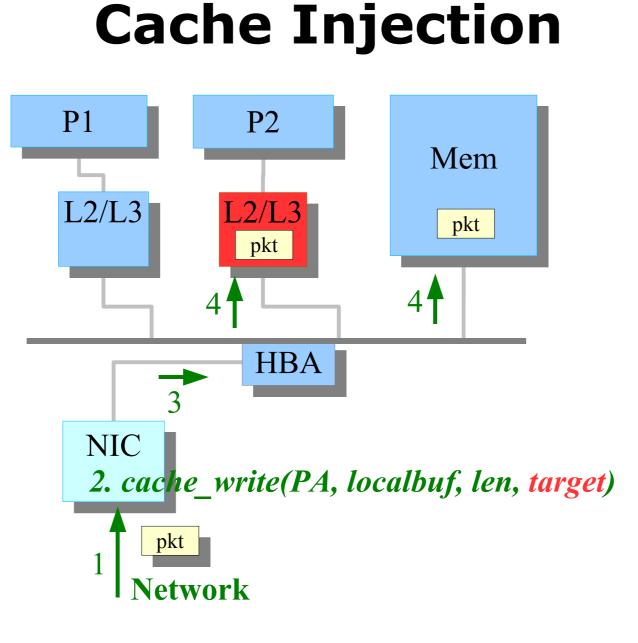
The Shim Layer

An Infrastructure for the Development of Kernel Network Services. Proof of Concept: Fast UDP

Edgar A. León University of New Mexico

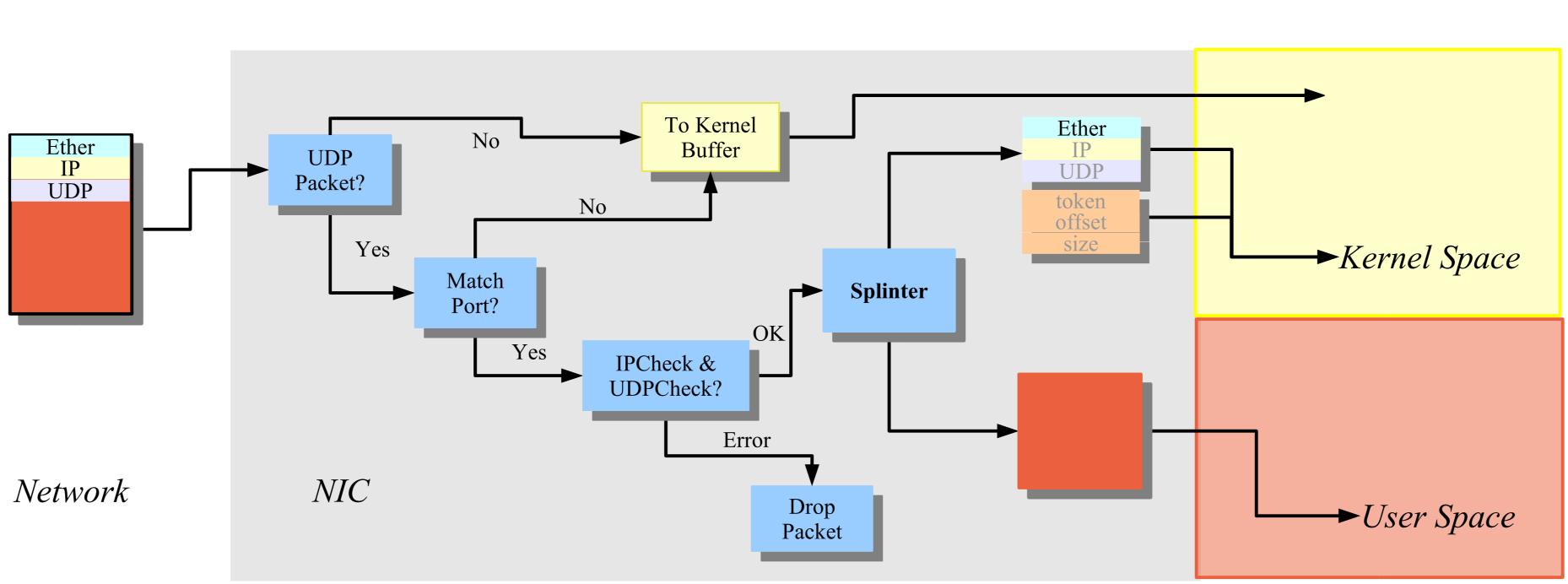
Shim Interface

- •mem write, cache_write
- •mem read, cache read
- •memmap_define
- memmap_delete
- set_memmap_io_funcs
- •schedule job
- •delay_cycles
- raise_interrupt

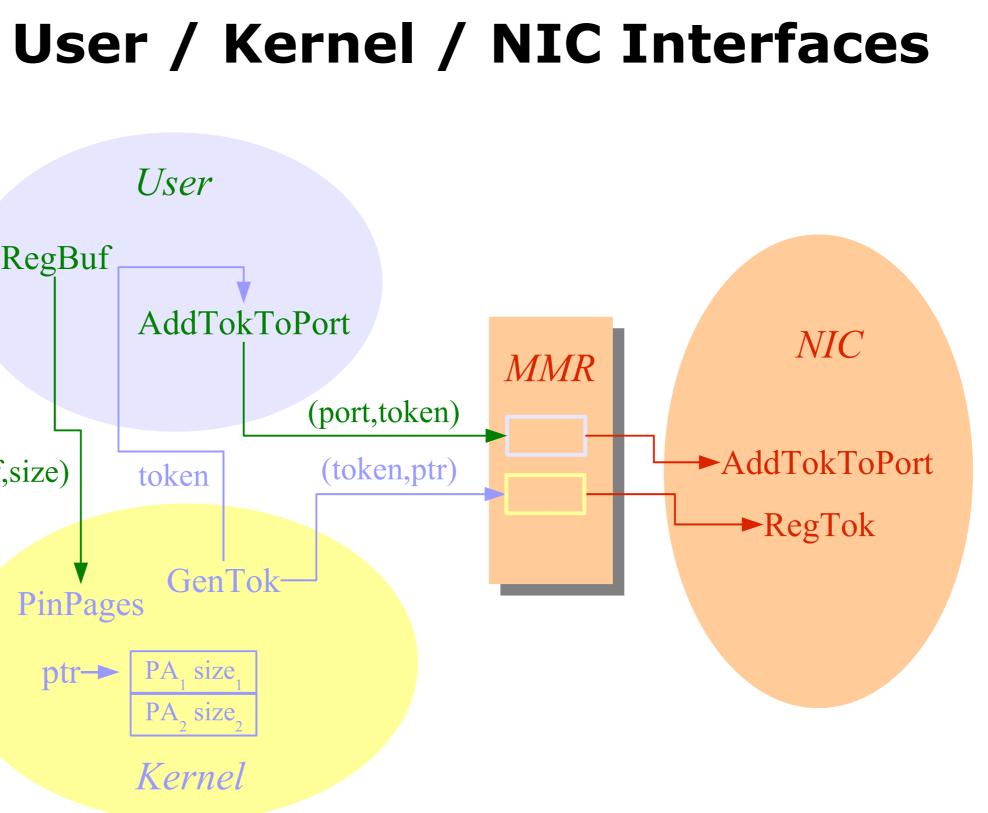


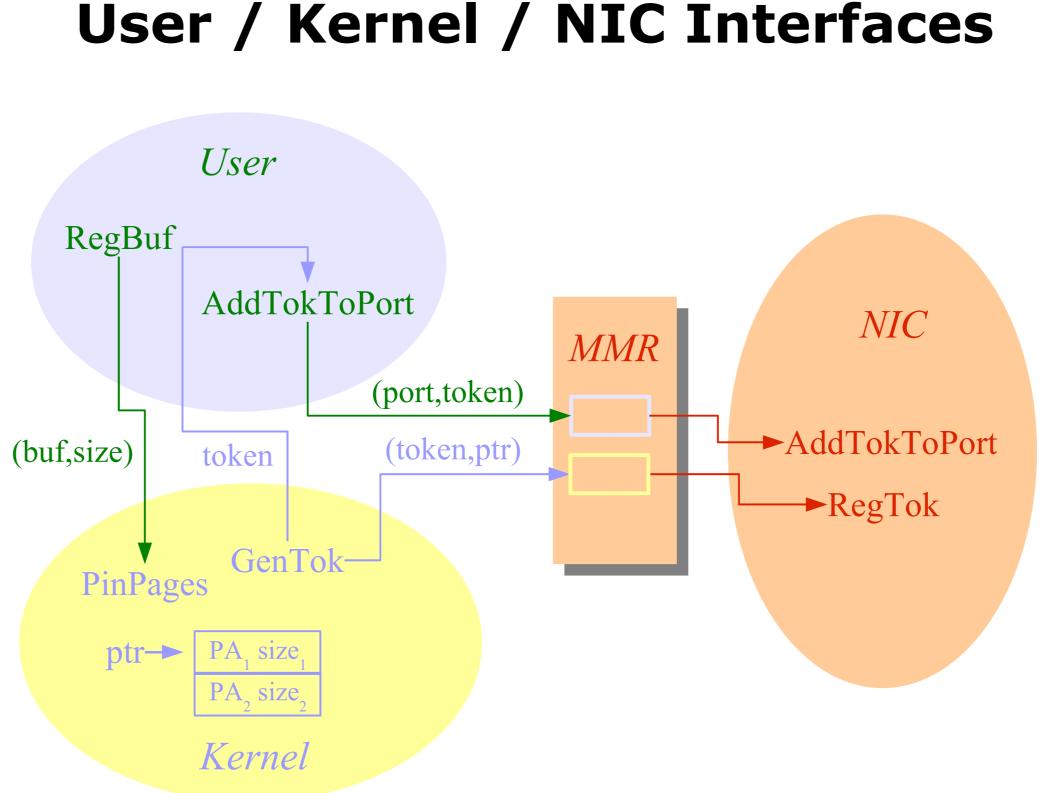


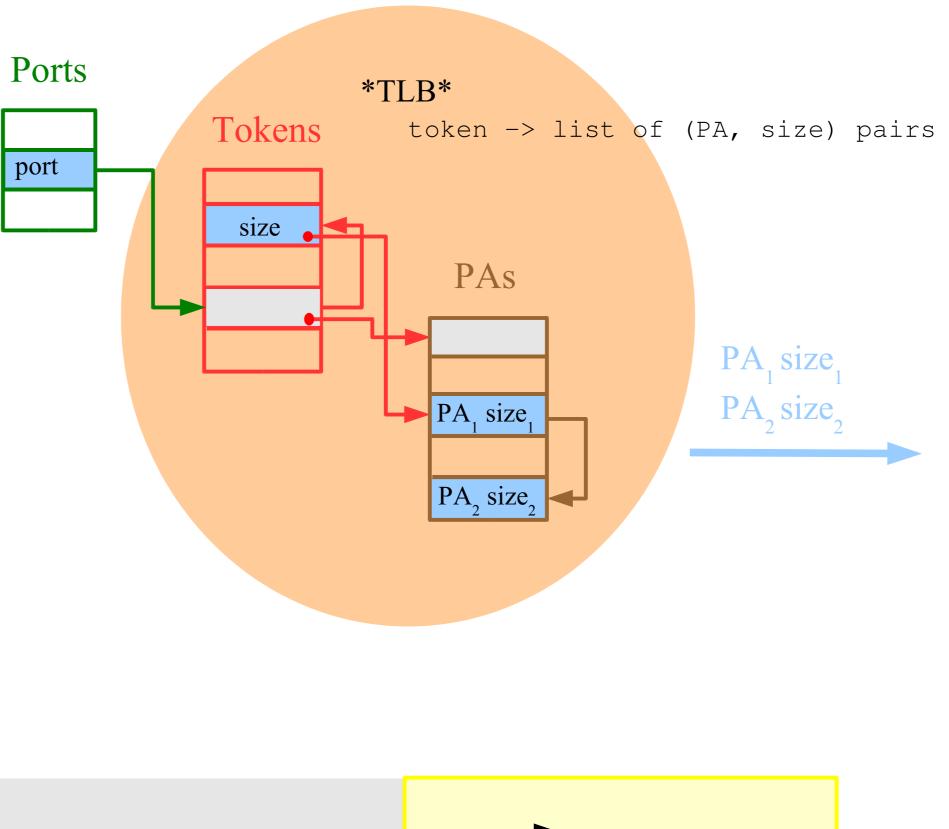
- *Splinter* data from control information
- Application's data bypasses the OS
- Delivery notification provided by the OS
- *Matching* on the NIC
- NIC has enough information to perform data placement directly
- NIC Offload
- Splintering, Message Matching, UDP/IP checksum semantics

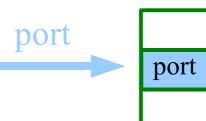




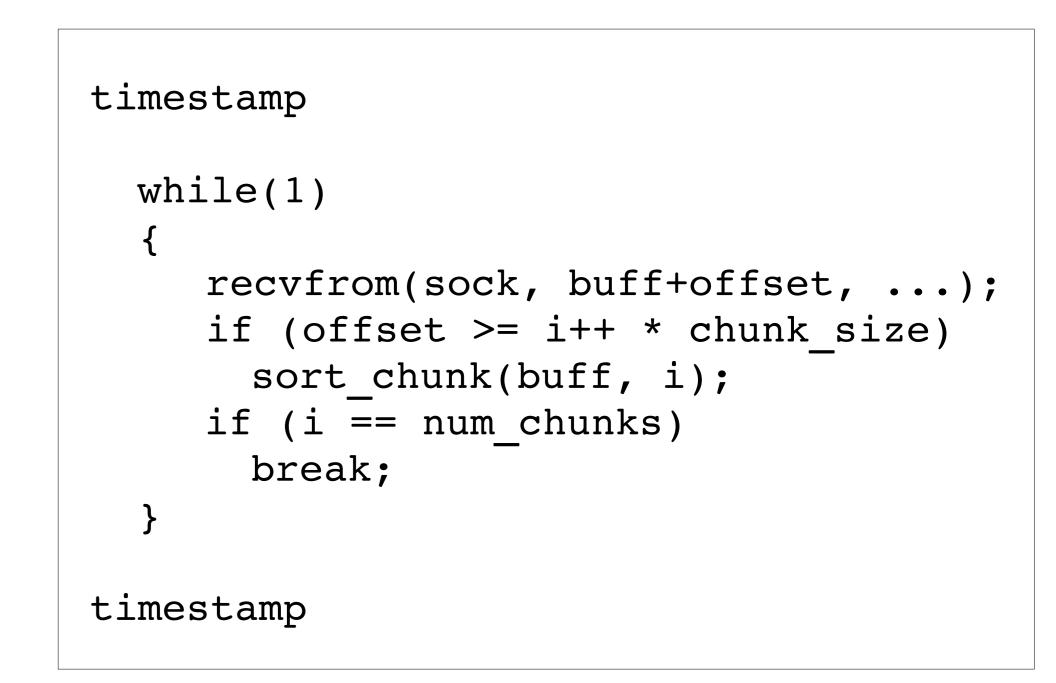




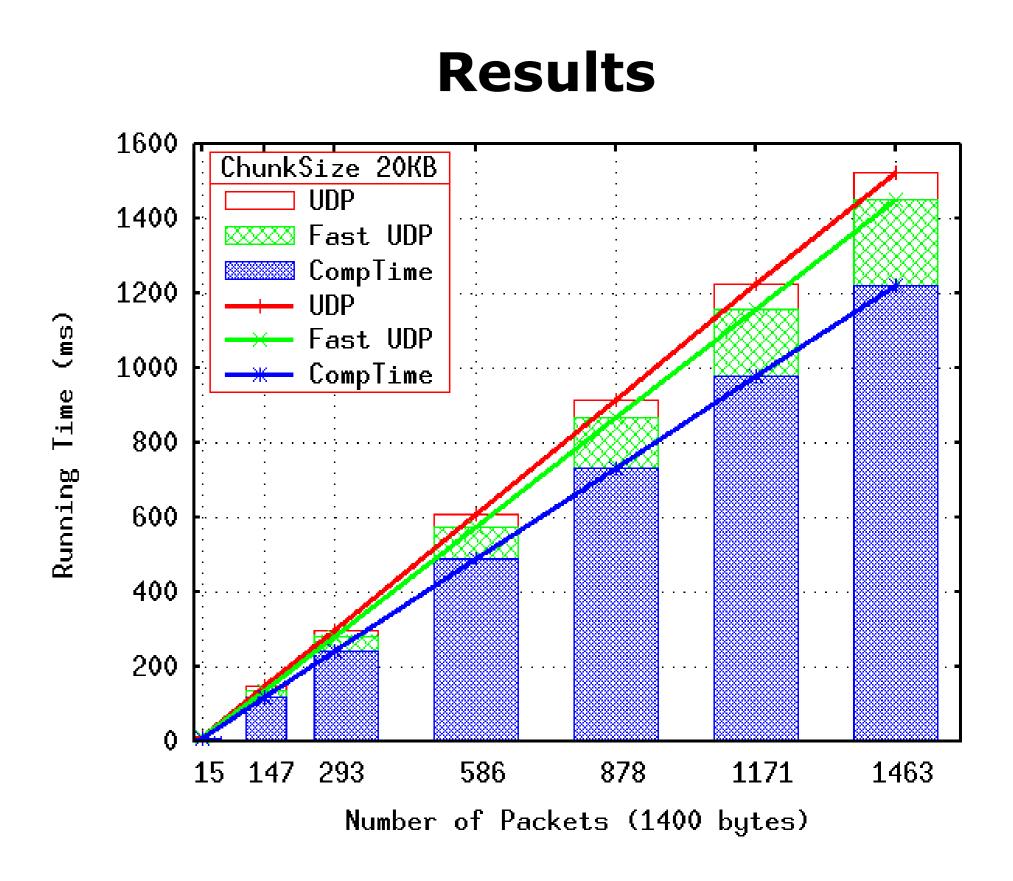




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NIC Data Structures and Matching



Conclusions and Future Work

- Developed an infrastructure to:
 - Better understand the interactions between smart NICs, the OS, and applications

 - Make a case for kernel network services that improve application's performance
- Proof of concept: Fast UDP • 5% improvement on an 80% computation-bound application
- Future Work
- HPC applications
- Study OS services to leverage cache injection for
- Study functionality placement of these services between NIC and OS







Test Application

• Study recent and future NIC architectures